Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	60106	(integrated adj circuit or IC) same test$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 00:36
L2	6535	1 and (extern$4 or output) near4 pin	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 00:37
L3	272	2 and input near5 buffer same connect$4 near5 pin	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 00:37
L4	62	3 and (output near4 terminal and test$4 near4 mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 00:38
L5	38	4 and input same disabl$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 00:39
L6	5	5 and mode adj input near4 terminal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 00:39